# Electronic Acknowledgement Receipt FES ID: 1042895 Application Number: 10776101 Confirmation Number: 2835 High speed power-gating technique for integrated circuit devices Title of Invention: incorporating a sleep mode of operation First Named Inventor: Kim C. Hardee Customer Number: 25235 Filer: William J. Kubida/Julie Lange Filer Authorized By: William J. Kubida Attorney Docket Number: UMI-360 Receipt Date: 09-MAY-2006 Filing Date: 11-FEB-2004 Time Stamp: 16:59:13 Application Type: Utility International Application Number: Payment information:

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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		DOC025.PDF	70549	yes	11

Multipart Description					
Doc Desc	Start	End			
Amendment - After Non-Final Rejection	1	1			
Specification	2	2			
Amendment Copy Claims/Response to Suggested Claims	3	6			
Drawings	7	7			
Applicant Arguments/Remarks Made in an Amendment	8	10			
Drawings	11	11			

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.